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ELECTROPLATING

Field of the Invention

The invention relates to electroplating, and in particular to electroplating of printed circuit boards (PCBs) and printed wiring boards (PWBs). In one particular aspect the invention relates to electroplating through-holes and blind holes in PCBs/PWBs.

Background to the Invention

Holes in PCBs/PWBs provide two primary functions. Firstly, they accommodate mounting pins for electronic components, and secondly, they provide routing paths between circuit on multi-layer printed circuit boards. Typically these holes can generally be referred to as through-holes and blind holes(or in the case of PWBs, Vias and blind vias). A layer of copper is deposited within the holes by electroplating. There are a number of well-establish techniques within the PCB manufacturing industry for copper electroplating the holes in PCBs. These typically involve a large number of chemical processes before the final electroplating process and such are well within the acknowledge of the skilled addressee.

Known electroplating techniques employ both direct current (DC) and a more recently periodic pulse reverse currents. Although the latter method can eliminate some of the plating defects such as such as low throwing power (the ratio of plating

thickness/distribution on the PCB surface to the plating thickness/distribution within the holes), poor distribution across the PCB surface, poor levelling, plating nodules (resulting in a rough finished surface), poor uniformity of copper grain structure and inability to electro-deposit on an even deposit in the holes, some of these defects still occur in plating vias in the micro range.

One particular problem associated with known electroplating processes is the tendency of electrical charges to build up on any imperfections or high-spots on the object/surface being plated. This increased electrical charge attracts more copper than the surrounding areas, which results in a poor finish to the surface of the copper.

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In PCB manufacturer it is desirable to use a thick dielectric layer, and thus a thick PCB board, to ensure better functional and electrical performance. This results in high aspect ratio through-holes. A further problem with known electroplating techniques is achieving good plating distribution and a defect-free deposition within the high aspect ratio holes. Holes also represent an "imperfection" in the PCB surface and are prone to a build-up of copper as mentioned earlier.

25 Summary of the Invention

It is an object of the present invention to provide a method of electroplating that provides good plating distribution, particularly within through-holes and blind holes. A further object of the present invention is to at least ameliorate the above-mentioned disadvantages with known electroplating techniques, or at least to provide the public with a useful alternative.

According to a first aspect of the invention there is provided a method of electroplating wherein a complex current waveform is passed between an anode and an object being electroplated. The waveform is a cyclic alternating type having two portions, a positive portion including one or more spikes and a negative portion.

Preferably, the positive portion is a triangular shape.

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Preferably, the positive portion has a peak value of substantially 5ASD, the spikes have peak values of substantially 11.25ASD, and a negative portion has a peak value of substantially 12ASD.

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According to a first aspect of the invention there is provided a method of electroplating an object including:

providing a electroplating bath solution with one or more anodes therein,

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disposing an object to be electroplated in the bath, and passing a complex current waveform between the anode nodes and the object.

The waveform is a cyclic alternating type having two portions, a positive triangular shaped portion including one or more spikes and a negative portion.

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Preferably, the method further includes vibrating the object and/or agitating the bath solution.

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Further aspects of the invention will become apparent from the following description, which is given by way of example only.

Brief Description of the Drawings

Embodiments of the invention will now be described by way of example only and with reference to the accompanying drawings and table in which:

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Figure 1 illustrates a schematic of an electroplating system,

Figure 2 illustrates the Square wave (Waveform 1),

Figure 3 illustrates the Triangular wave of 2ASD with 12ASD

20 reverse current density (Waveform 2),

Figure 4 illustrates the Triangular wave of 2ASD with multipeaks and 12ASD reverse current density (Waveform 3),

Figure 5 illustrates the Triangular wave of 5ASD with 11.25ASD multi-peak and 12ASD reverse current density (Waveform 4), and

25 Table 1 is a summary of the throwing power and hole distribution results for Waveforms 1 to 4. 10

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Description of the Preferred Embodiments

Referring to Figure 1, an electroplating system includes a tank or bath 1 containing electroplating solution 2. Disposed within the solution 2 at convenient locations are anodes 3. The solution and anode material are of a type suitable for the electroplating process. Such are well known in the art. For copper electroplating the solution 2 might be copper sulphate with copper anodes 3. The solution 2 may also contain a brightener and carrier in known manner.

An object 4, which is to be electroplated, is disposed within the tank and solution 2, and forms the cathode. A current source 5 provides an electrical current which is passed through the solution 2 between the anodes 3 and object 4.

The present invention uses complex current waveforms in the electroplating process instead of a single type of DC or pulse waveform. Complex waveforms are alternating waveforms of an arbitrary shape whose form changes with time during the plating process. In the preferred embodiment of the invention the complex waveforms are cyclic with a triangle positive portion and a square negative portion. The positive portion has multiple positive spike currents.

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The waveform of the preferred embodiment is shown in Figure 5.

It comprises 6 current spikes of magnitude 11.25ASD superimposed

in a positive triangular waveform with an initial peak magnitude of 5ASD and a period of 1 millisecond. The negative portion of the cycle is a square waveform of 12ASD magnitude and 1 millisecond period.

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The preferred embodiment also includes a vibrator 6 to vibrate the object 4 and an agitator 7 to agitate the electroplating solution 2. In the preferred embodiment the agitator may be air agitation.

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The benefits of the current invention are illustrated by the following five tests, which are given by way of example only. The waveforms of the tests are shown in Figures 2 to 5. The test apparatus was an electroplating system substantially as that shown in Figure 1, using a Printed Wiring Board as object 4. The results are shown in Table 1, which compares Throwing Power and distribution with a blind Via for each waveform tested.

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20 A typical square waveform (Figure 2) and two triangular waveforms (Figure 3 and 4) were used for copper plating blind Vias with diameters ranging from 50 and 150µm in a FWB and the typical square wave was used as a control. The throwing power and the copper distribution inside the Via were compared in the 25 tests and shown in Table 1.

The triangular wave (Waveform 2) and triangular wave with spike (Waveform 3) show improved throwing power in both 50, 75, 100 and 150-micron microvias. Waveform 3 has high throwing power and a good profile of copper deposit inside the via.

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Better agitation of the electroplating solution can improve the throwing power. Vibration of the object is further applied to the Waveform 4. The results are shown in Table 1. The trial with the triangular wave 5ASD with 11.25ASD multi-peak and 12ASD reverse current density did not show improvement in throwing power as compared with Waveform 3, but a very good profile of copper deposit inside the 50-micron via was obtained and the base of the 50-micron via was completely covered with copper deposits.

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Embodiments of the invention have been described, however it is understood that variations, improvement or modifications can take place without departure from the sprite of the invention or scope of the appended claims.